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ISSUE CLASSIFICATION	
Class	Subclass

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PATENT DATE

APPLICATION NO. 09/819940	CONT/PRIOR	CLASS 348 375	SUBCLASS 240.16	ART UNIT 2613	EXAMINER Diep
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APPLICANTS

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Coprocessor circuit architecture, for instance for digital encoding applications

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.		CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED	

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